

In the Specification:**Clean Version of the Amended Section(s) of the Specification**

Page 6, lines 9-10:

B1
Figure 2 is a block diagram which shows a host computer connected to three independent modems via a common bus and three interface ports.

Page 15, line 15 through page 16, line8:

To initiate a modem connection, the host computer 22 establishes a link with one of the modem channels of the modem 24, for example by sending command information to the transmit FIFO 44 and the channel number information corresponding to the relevant command logical channel to the mailbox interface 46. In conventional networks, the terminal 58 connected to the modem via the host computer 22 operates as if it were connected directly to the modem 24. Accordingly, the terminal 58 may attempt to send a command directly to the modem 24 instead of requesting the host computer 22 to send the command. A command initiated by the terminal 58, however, may be provided in a conventional manner, for example by asserting an escape sequence in the data provided to the modem 24, instead of sending a command on the appropriate command logical channel. Consequently, a communication system 300 according to various aspects of the present invention monitors the contents of the RAM 30 allocated to each logical channel for conventional escape sequence detection schemes. For example, the modem 24 may operate in conjunction with an improved escape detection scheme as disclosed in United States Patent Application Serial No. 09/264,777, entitled "Method for Reducing CPU Burden in Monitoring Data Traffic for Escape Sequences", filed March 9, 1999.
B2

Marked-Up Version of the Amended Section(s) of the Specification

Page 6, lines 9-10:

Figure 2 is a block diagram which shows a ~~modem~~ host computer connected to three independent modems via a common bus and three interface ports.

Page 15, line 15 through page 16, line8:

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States Patent Application Serial No. _____ 09/264,777 ef

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